Cartsten Trinitis

Carsten Trinitis received his Ph.D. in Electrical Engineering in 1998 from Technische Universität München, Germany. He developed an automatic optimization system for three dimensional high voltage apparatus with a parallel implementation of electrostatic field simulation on workstation clusters. In 1998, he held a postdoc position at Lehrstuhl für Rechnertechnik und Rechnerorganisation, Computer Science Department Technische Universität München and joined FORCE Computers GmbH, Neubiberg, Germany, as an application engineer in 1999, where he concentrated on the standardization of High Availability systems for telecommunications. In 2001, he returned to TU München, where he now holds a position as senior scientist and is heading a research group on parallel and distributed computer architectures. Since 2002, he is also an assistant professor for history of science at Universität der Bundeswehr München, Germany.

His research interests comprise parallel computer architectures, microprocessor architectures, multi- and many-core architectures as well as the adaptation of numerical simulation codes to these architectures.

Carsten is a member of the Munich Multicore Initiative (MMI), a reasearch group at Technische Universität München, dealing with performance issues realted to multi- and many-core architectures.

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